

EmRunner: CAN support in Linux

EmRunner's CAN registers are accessed in two steps:

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1. put register address on the wire
2. put/read data to/from register

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That is the reason, why standard [LinCan](#) driver is not working as is. Following changes have to be made to make the driver working.

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Introduce such a global variable:

```
can_ioptr_t base_addr;
```

```
in gensjal000io_init_chip_data() base_addr should be the value of chip_base_addr.
```

So write routine in gensjal000io.c would look like this:

```
gensjal000io_write_register( data, address)
{
    can_outb(address - base_addr, base_addr); // calculate register offset
    can_outb(data, base_addr + 1);
}
```

Read routine would look like this:

```
gensjal000io_read_register( address)
{
    can_outb(address - base_addr, base_addr); // calculate register offset
    can_inb(data, base_addr + 1);
}
```

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CAN reset is to be handled the same way.

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